

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Takashi KUROI ET AL.

SERIAL NO: NEW APPLICATION

GAU:

FILED: HEREWITH

EXAMINER:

FOR: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references were submitted in prior application Serial No. 09/401,849 filed September 22, 1999, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- Each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



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David A. Bilodeau
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CERTIFICATION

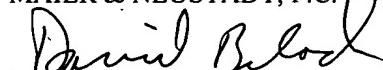
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Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 244521US-2 DIV		SERIAL NO. NEW APPLICATION	
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT		Takashi KUROI ET AL.			
		FILING DATE HEREWITH		GROUP			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	6,515,338	02/04/03	INUMIYA ET AL.			
	AB	6,104,077	08/15/00	GARDNER ET AL.			
	AC	6,242,776	06/05/01	HAUSE ET AL.			
	AD	6,064,107	05/16/00	YEH			
	AE	5,834,816	11/10/98	JANG			
	AF	6,025,635	02/15/00	KRIVOKAPIC			
	AG	5,880,508	03/09/99	WU			
	AH	5,828,130	10/27/98	MILLER			
	AI	5,600,168	02/04/97	LEE			
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION		
					YES	NO	
	AO	11-26756	01/29/99	JAPAN			XX
	AP	11-74503	03/16/99	JAPAN			XX
	AQ	10-135482	05/22/98	JAPAN W/ENGLISH ABSTRACT			XX
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW						
	AX						
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner					Date Considered		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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U.S. PATENT DOCUMENTS

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	AA						
	AB						
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	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	NO
	AO	8-37296	2/6/96	Japan (with English Abstract)		X
	AP	7-99310	4/11/95	Japan (with English Abstract)		X
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

AW	Atsushi YAGISHITA, et al., "High Performance Metal Gate MOSFETs Fabricated by CMP for 0.1 μm Regime," International Electron Devices Meeting, (1998), Pgs. 785-788.
AX	A. CHATTERJEE, et al., "CMOS Metal Replacement Gate Transistors using Tantalum Pentoxide Gate Insulator," International Electron Devices Meeting, (1998), Pgs. 777-780.
AY	
AZ	

Examiner

Date Considered

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